



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

e Application of:

KRISHNAPPA ET AL.

Filed:

DECEMBER 15, 2003

For:

APPARATUS AND METHODS FOR CONFIGURATION OF

PROGRAMMABLE LOGIC DEVICES

Serial No.:

10/736,167

Group Art Unit:

2819

Examiner:

CHANG, D.

Atty. Dkt.:

ALTR:024

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

Date

Name

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449, as well as the International Search Report, be considered by the Examiner and made of record.

Copies of the listed documents required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as

defined in 37 C.F.R. § 1.56(b).

Pursuant to Rule 97, applicant states that each item in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to filing of this statement.

No fees are believed to be due in connection with the filing of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Deposit Account No. 10-1205/ALTR:024.

Applicant respectfully requests that the listed document(s) be made of record in the present case.

Respectfully submitted,

Maximilian R. Peterson

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Enclosures

Form PTO-1449 (modified)	Atty. Docket No. ALTR:024	Serial No. 10/736,167
List of Patents and Publications for PE INFORMATION DISCLOSURE	KRISHNAPPA ET AL	. •
JUL 2 7 2005 (Use several sheets if neces	Filing Date:	Group: 2819
U.S. Patent Documents See Page 1	Foreign Patent Documents See Page	Other Art See Page 1

U.S. Patent Documents							
Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	Al	6,507,943	1/14/03	Kelem			9/26/00

Foreign Patent Documents							
Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						

C	Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)				
Exam. Init.	Ref. Des.	Citation			
	C1	Leventis et al., "Cyclone: A Low-Cost, High-Performance FPGA", IEEE Custom Integrated Circuits Conference, Sept. 21-24, 2003, pps. 49-52.			
	C2	"Cyclone Device Handbook, Vol. 1, Ch. 13, "Configuring Cyclone FPGAs", Altera Corporation, July 2003, pps. 1-56.			
	C3	International Search Report, PCT/US2004/042064, ALTR:024PCT, 13 pgs.			

Examiner:

Date Considered:

EXAMINER: initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.